

# AHG5 DS-1 TIMING DISTRIBUTOR DATA SHEET SYNCHRONIZATION DISTRIBUTION EXPANDER

## GENERAL

The DS-1 timing distributor plug-in unit, AHG5, is used in the SDE (Synchronization Distribution Expander) to provide an 'all ones' framed DS-1 signal with selectable D4 or ESF (Extended Super Frame) type framing. The plug-in unit can be inserted into any TD (timing distributor) card slot and is compatible with current backplane wiring.

## FEATURES

The DS-1 Timing Distributor, AHG5 has:

- Ten framed DS-1 outputs
- Selectable D4 or ESF format
- "Cutoff" capability.

## DESCRIPTION

The input signal to the plug-in unit is a dual-rail unipolar composite clock signal received from the TI (timing interface) plug-in units. This input signal is recovered and the signal quality is monitored by single-rail failure detectors. A 64-KHz square wave is derived from the input and then multiplied by a phase-locked loop to a frequency of 4.096 MHz. The 4.096 MHz is used to synchronize the DS-1 signal generating circuitry. The DS-1 generator has an option selector for ESF or D4 type framing on the 'all ones' DS-1 output. The output of the DS-1 generator is fed to an output driver circuitry consisting of ten individual transformer coupled outputs. Each output is equalized to meet the DS-X cross-connect specification of 655 feet. The cabling distance from the SDE to the equipment requiring timing is 1140 feet [400 feet (from SDE to the DS-X cross-connect) + 85 feet (at the DS-X cross-connect)

+ 655 feet (from the DS-X to NE (network elements) needing timing)].

Besides the D4 or ESF format select switch, an output "cutoff" switch is also available on the TD. This switch allows the SDE to synchronize equipment of a higher stratum level than the SDE, without violating the rules of stratum hierarchy. In the event of a TI dual-input failure, the TD disables its outputs and allows the synchronized equipment to hold-over at its prescribed stratum level.

## OPTIONS

There are two options available on AHG5 plug-in unit. The first option selects the framing pattern type (D4 or ESF). The option selection switch is shown in Figure 1. The switch section labeled "1" is used for framing selection. The switch in the "D4" position selects the D4 framing format and in the "ESF" position the ESF framing format.

The second option selects a "cutoff", which is used when supplying timing signals to the network elements of a higher stratum. The rules for stratum timing state that the network elements may only receive timing from a source of equal or higher stratum. However, if the "cutoff" option is used, the outputs of the AHG5 plug-in unit are inhibited if both inputs to the SDE become invalid. In this way, the network elements receiving timing from the SDE recognize that the input has failed and rely on their own internal clocks for holdover during this outage. Upon restoration of at least one input to the SDE, the outputs of the AHG5 plug-in unit are again activated and provide a valid output once more. If the "cutoff" option is not selected, the AHG5 plug-in unit outputs provide a signal as accurate as the inputs from the timing interfaces

during a dual-input failure. The "cutoff" option is selected by choosing the proper position of the switch shown in Figure 1. The switch section labeled "2" is used. The switch in the "IN" position selects the "cutoff" option and in the "OUT" position, inhibits the "cutoff" option. To use the cutoff option, control wiring from the timing interface plug-in units may be necessary and is shown in Table A.

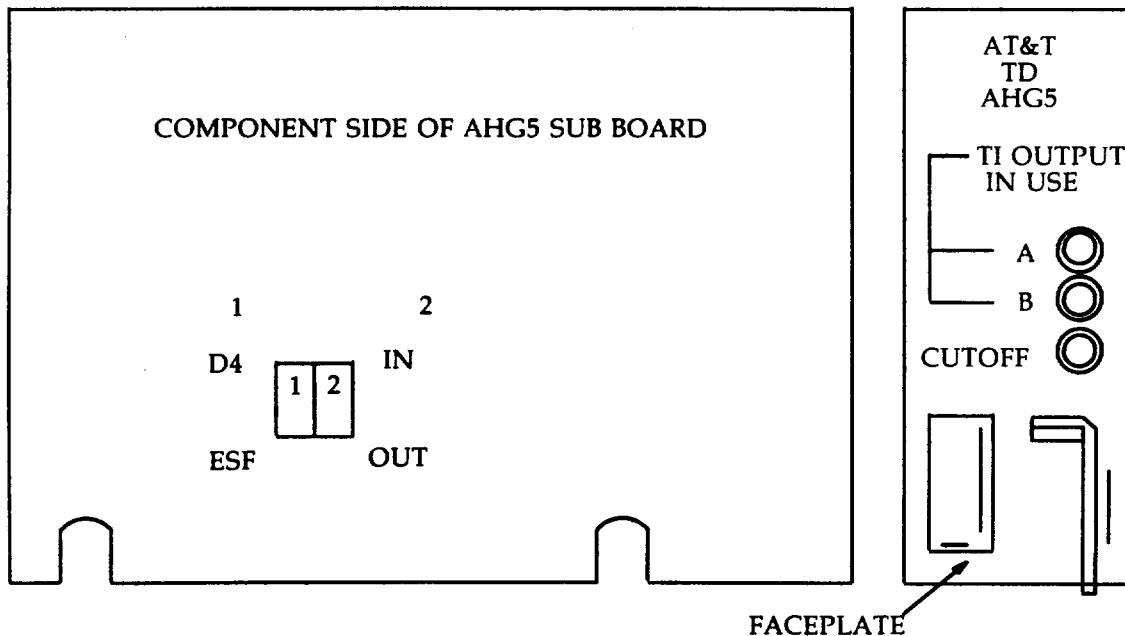


Figure 1. Location of Option Switches on AHG5 Timing Distributor Plug-In Unit

**Warning:** This plug-in unit contains devices that are subject to damage or decreased reliability from static discharges. When handling this unit, proper anti-static measures should be taken, such as wearing grounding bracelets and handling by the faceplate only.

### INSTALLATION

Before the AHG5 plug-in unit may be inserted into the SDE shelf, the proper options must be set as described above. Also, the addition of grounding wires may be needed for the J98726W-1 and J98726W-2 panels only. These wires connect the shield row on the SDE terminal blocks to frame ground as described in Table B. These wires are attached to frame ground via backplane screws with wire-wrap lugs.

If the cutoff option is chosen, care must be exercised in installing the necessary backplane wiring as to prevent a minor disruption of service. Once the options are set, the AHG5 plug-in unit may be inserted in any primary TD slot (1, 2, 3, 4). Two additional operations must also be performed:

- (1) All unused AHG5 plug-in unit outputs must be terminated with a 100-ohm resistor across tip and ring at the appropriate SDE terminal block location. Resistors should be wire wrapped to the terminals.
- (2) Input and output shield connections should be made as short as possible.
- (3) The 100-ohm resistor must be removed before previously unused output may be used.

Output signal redundancy is easily achieved for equipment requiring multiple DS-1 input timing signals. The DS-1 timing signals should be taken from more than one AHG5 plug-in unit located in any TD slot. This arrangement ensures that one AHG5 plug-in unit may be removed at a time without disrupting service to the equipment receiving timing.

**REPLACEMENT**

The AHG5 plug-in unit can be replaced by simply removing the particular AHG5 plug-in and inserting the replacement AHG5 plug-in unit in the same slot. The PCS (parallel changeover system) cannot be used for AHG5 plug-in unit change. Therefore, the adjacent TD\_S slots should not be used in the panel J98726W-2, L3.

TABLE B (Note)		
SHIELD GROUNDING MODIFICATIONS		
Wire Position	FROM	TO
1	Lug No. 1	SDE A, A5
2	Lug No. 1	SDE A, A10
3	Lug No. 1	SDE A, A15
4	SDE A, A20	SDE B, A1
5	Lug No. 2	SDE B, A5
6	Lug No. 2	SDE B, A10
7	Lug No. 2	SDE B, A15

Note  
 This wiring is required on the J98726W-1, L1, L2 and J98726W-2, L3 panels only. Lug screws are in sequential order starting with Lug No. 1, located at the top backplane screw between J5 and J6; lug 2 is located above J8. All wires should maintain between 0.5 and 0.75 inches of slack from point-to-point.

TABLE A (Note)				
CUTOFF OPTION WIRING				
PACK POSITION	FROM		TO	
	Slot	Terminal	Slot	Terminal
TI A	J2	16	J2	43,44,45
TI B	J3	16	J3	43,44,45
TD 1	J4	44	J4	42
TD 1	J4	45	J4	43
TD 2	J5	44	J5	42
TD 2	J5	45	J5	43
TD 3	J6	44	J6	42
TD 3	J6	45	J6	43
TD 4	J7	44	J7	42
TD 4	J7	45	J7	43

Note  
 This wiring is required on the J98726W-1, L1, L2 and J98726W-2, L3 panels only.

**SPECIFICATIONS**

The acceptable limits are as follows:

**General:**

- Max. Output Distance = 1140 feet (400+85+655) per Compatibility Bulletin 119.
- Output Equalization = 655 feet
- Output Range from DSX = 0 to 655 feet
- Output Impedance = 100-Ohm nominal
- Output Framing Format = D4/ESF selectable

**Temperature:**

- +40 to +100 degrees F - long term
- +4.4 to +37.8 degrees C

- +35 to +120 degrees F - short term
- +1.6 to +49 degrees C

**Humidity:**

- Min. Relative Humidity Range**
- 20 to 80 percent - short term
- 20 to 55 percent - operating

**ORDERING INFORMATION**

The AHG5 TD may be ordered as a single plug-in unit using the code given below:

<b>PLUG-IN UNIT</b>	<b>CLEI CODE</b>
AHG5	D4PQ110AXX

**REFERENCES**

The following publications provide more information on the SDE/CDU.

- SD-7C389-01
- SD-7C389-02
- AT&T Practice 314-813-101 - CDU - Description and Operation
- 314-813-102 - Data Sheet - AHG16 Timing Interface (CDU)
- AT&T Practice 314-913-220 - Description and Operation
- AT&T Practice 314-913-221 - Installation and Maintenance Procedures
- 314-913-222 - Data Sheet - AHG1 Timing Alarm
- 314-913-223 - Data Sheet - AHG4 Timing Distributor (CDU/SDE)
- 314-913-224 - Data Sheet - AHG10 Timing Interface
- 314-913-228 - Data Sheet - AHG25 Timing Distributor (CDU/SDE).

**GLOSSARY**

The acronyms used in this data sheet are listed and defined below.

D4	D4 Framing Format
ESF	Extended Superframe Format
NE	Network Element
PCS	Parallel Changeover System
SDE	Synchronization Distribution Expander
TA	Timing Alarm
TD	Timing Distributor
TI	Timing Interface.